| Electronic Patent Application Fee Transmittal | | | | | | | |
|---|---|----------|----------|--------|-------------------------|--|--|
| Application Number: | 10699764 | | | | | | |
| Filing Date: | 03-Nov-2003 | | | | | | |
| Title of Invention: | In-circuit configuration architecture for embedded configurable logic array | | | | | | |
| First Named Inventor/Applicant Name: | Albert Sun | | | | | | |
| Filer: | Mark Andrew Haynes/diane bergin | | | | | | |
| Attorney Docket Number: | MXIC 1520-1 | | | | | | |
| Filed as Large Entity | | | | | | | |
| Utility Filing Fees | | | | | | | |
| Description | | Fee Code | Quantity | Amount | Sub-Total in USD(\$) | | |
| Basic Filing: | | | | | | | |
| Pages: | | | | | | | |
| Claims: | | | | | | | |
| Miscellaneous-Filing: | | | | | | | |
| Petition: | | | | | | | |
| Patent-Appeals-and-Interference: | | | | | | | |
| Post-Allowance-and-Post-Issuance: | | | | | | | |
| Statutory disclaimer | | 1814 | 1 | 130 | 130 | | |
| Extension-of-Time: | | | | | | | |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|----------------|-------------------|----------|--------|-------------------------|
| Miscellaneous: | | | | |
| | Total in USD (\$) | | | 130 |